

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	224	du.xa.	USPAT	OR	OFF	2004/09/03 14:36
S2	25	"6073254"	USPAT	OR	OFF	2004/11/27 11:07
S3	36	"sequence manager"	USPAT	OR	OFF	2005/04/21 15:56
S4	0	S3 same (logic adj gate\$2)	USPAT	OR	OFF	2004/11/27 11:17
S5	15	"sequence manager"	USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/27 12:12
S6	75083	input with (equal\$3 equivalent) with output	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/27 12:17
S7	1807	input with (equal\$3 equivalent) with output with mode	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/27 12:18
S8	56	S7 same sequence	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/27 12:35
S9	742226	'pass through'	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/21 15:15
S10	8246	'pass through' near2 signals	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/27 12:36
S11	1066	S10 with control\$3	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/27 12:37
S12	72	S11 same gates	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/27 12:38
S13	6	S11 same gates same mode	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/27 12:38

S14	1089	("integrated circuit" ic) with test\$3 with individual\$2	USPAT	OR	OFF	2005/04/21 13:29
S15	31	("integrated circuit" ic) with test\$3 with (block\$1 near3 individual\$2)	USPAT	OR	OFF	2005/04/20 16:09
S16	1	S15 same transparent	USPAT	OR	OFF	2005/04/20 16:09
S17	21	("integrated circuit" ic) with (test\$3 near3 mode) with individual\$2	USPAT	OR	OFF	2005/04/21 13:30
S18	7978	'pass through' with enable	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/21 15:15
S19	35	'pass through' near5 (enable adj signals)	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/21 15:16
S20	0	S19 with mode	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/21 15:16
S21	1	"sequence manager" near3 mode	USPAT	OR	OFF	2005/04/21 15:58
S22	1717	(portion selected) near2 signals with mode	USPAT	OR	OFF	2005/04/21 15:59
S23	7	(portion selected) near2 "enable signals" with mode	USPAT	OR	OFF	2005/04/21 16:42
S24	1	"4833655".pn.	USPAT	OR	OFF	2005/04/21 16:42
S25	1447	transparent near2 (latch buffer)	USPAT	OR	OFF	2005/04/21 16:43
S26	275	S25 same mode	USPAT	OR	OFF	2005/04/21 16:43
S27	166	S25 with mode	USPAT	OR	OFF	2005/04/21 16:51
S28	15	S27 same test\$3	USPAT	OR	OFF	2005/04/22 10:34
S29	1475	transparent near2 mode	USPAT	OR	OFF	2005/04/22 10:35
S30	7561	sequen\$4 near2 mode	USPAT	OR	OFF	2005/04/22 10:35
S31	21	S29 same S30	USPAT	OR	OFF	2005/04/22 10:35
S32	1933	transparent near2 mode	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/22 10:35
S33	8840	sequen\$4 near2 mode	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/22 10:35

S34	23	S32 same S33	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/22 15:36
S35	35	("4833655").URPN.	USPAT	OR	OFF	2005/04/22 11:06
S36	27559	logic near2 gates	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/22 15:37
S37	24	logic near2 gates with transparent	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/22 15:53
S38	1933	transparent near2 mode	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/22 15:53
S39	8840	sequen\$4 near2 mode	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/22 15:53
S40	0	S37 same S38 same S39	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/22 15:53
S41	23	S38 same S39	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/22 15:54
S42	0	S36 same S41	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/22 15:54
S43	0	S36 and S41	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/22 15:54